

10/544787

PATENT

JC20 Rec'd PCT/PTO 05101120 08 AUG 2009

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Pere ROCA I CABARROCAS et al.

Conf.

Application No. NEW NATIONAL PHASE

Group

Filed August 8, 2005

Examiner

TRANSISTOR FOR ACTIVE MATRIX DISPLAY AND A METHOD FOR PRODUCING  
SAID TRANSISTOR

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 8, 2005

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begin on page 3 of this paper.

Remarks begin on page 8 of this paper.

An Appendix is attached following the signature page of this paper.